

PATENT

Atty. Dkt. No. YOR920030591US1

**REMARKS**

In view of the above amendment and the following discussion, the Applicants submit that none of the claims now pending in the application are unpatentable under the provisions of 35 U.S.C. § 101 or made obvious under the provisions of 35 U.S.C. § 103. Thus, the Applicants believe that all of these claims are now in allowable form.

**I. REJECTION OF CLAIMS 1-19 UNDER 35 U.S.C. § 101**

The Examiner has rejected claims 1-19 in the Office Action under 35 U.S.C. §101 as claiming the same invention as that of claims 1-20 of US Patent 6,898,261 (Hunter et al.). Applicants respectfully traverse the rejection. The Examiner's attention is directed to the fact that claims 1-19 of the present invention are not identical to claims 1-20 of US Patent 6,898,261. As such, Applicants respectfully submit that the present statutory double patenting rejection is improper. Applicants submit that claims 1-19 are patentable over Hunter et al. and respectfully request the rejection be withdrawn.

**II. REJECTION OF CLAIMS 20-26 AND 28-34 UNDER 35 U.S.C. § 103**

The Examiner has rejected claims 20-26 and 28-34 in the Office Action under 35 U.S.C. § 103 as being unpatentable over Singh et al. (U.S. Patent 6,026,354, issued on February 15, 2000) in view of Wiedenman, (U.S. Patent 7,051,131, issued on May 23, 2006). Applicants respectfully traverse the rejection.

Singh teaches a device monitoring system with a dedicated bus and processor. Wiedenman teaches a method and apparatus for recording bus activity.

The Applicants respectfully submit that Singh and Wiedenman, alone or in any permissible combination, fail to teach or suggest a method and digest collector for monitoring event occurrences from a plurality of functional processor units at a centralized location by storing data indicative of cumulative events in a register based on a tag identifier affixed to said data, as positively claimed by Applicants. Specifically, Applicants' independent claims 20 and 29 positively recite:

20. (Original) A method for monitoring event occurrences from a plurality of functional processor units at a centralized location via a dedicated bus coupled

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between said plurality of functional processor units and said centralized location, said method comprising:

- receiving, at said centralized location, data indicative of cumulative events occurring at one of said functional processor units;
- storing said data in a first temporary memory;
- storing said data in a register based on a tag identifier affixed to said data, said tag identifier providing indicia of one of said plurality of functional processor units. (Emphasis added.)

29. A digest collector for centrally monitoring event occurrences at a plurality of functional processor units, said digest collector comprising:

- a bus latch, said bus latch having an input adapted for coupling to a dedicated bus that is coupled to said plurality of functional processor units, said bus latch for collecting data associated with at least one of said plurality of functional processor units;
- a register file coupled to an output of said bus latch; and
- control circuitry, coupled to said bus latch and said register file, said control circuitry for controlling transfer of said data associated with at least one of said plurality of function processor units to said register file based on a tag identifier affixed to said data. (Emphasis added.)

In one embodiment, Applicants' invention teaches the use of a tag identifier that is affixed to the data. For example, each output value from the reducer 1010 is sent along with a tag. The tag is used to provide special instructions to the control logic 1208 of the digest collector 1130. The tag may comprise one or more bits that are appended or prepended to the output value from the reducer 1010. The control logic 1208 examines each tag and determines whether the output value should be stored. If the tag indicates that the output value from the reducer 1010 is to be stored (e.g., the bits are all set high), the control logic 1208 instructs the collector bus latch 1202 to send the output value (and not the tag) to the SPR file 1204 for storage. Otherwise, the output value is not stored in the SPR file 1204, and only remains in the collector bus latch 1202. (See Applicants' specification, Paragraph 0082).

In contrast, both Singh and Wiedenman fail to provide this teaching. First, the Examiner conceded on the record that Singh does not teach such a tag identifier affixed to the data. However, the Examiner then alleged that Wiedenman bridges this gap by citing Wiedenman's register base control. However, the Examiner's attention is directed to the fact that both Singh and Wiedenman fail to provide the teaching of a tag identifier that is affixed to the data.

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As such, the combination of Singh and Wiedenman fails to make obvious Applicants' independent claims 20 and 29.

In addition, dependent claims 21-26 and 28-34 depend from independent claims 20 and 29, respectively and recite additional limitations. As such, and for the exact same reason set forth above, the Applicants submit that claims 21-26 and 28-34 are also patentable over Singh and Wiedenman and respectfully request the rejection be withdrawn.

### III. OBJECTION TO CLAIM 27

Applicants express their appreciation that dependent claim 27 is allowable. Responsive to the Examiner, Applicants respectfully submit that dependent 27 is not dependent on a rejected independent claim for the reasons discussed above and respectfully request the rejection be withdrawn.

### Conclusion


Thus, the Applicants submit that all of these claims now fully satisfy the requirements of 35 U.S.C. §101 and §103. Consequently, the Applicants believe that all these claims are presently in condition for allowance. Accordingly, both reconsideration of this application and its swift passage to issue are earnestly solicited.

If, however, the Examiner believes that there are any unresolved issues requiring the issuance of a final action in any of the claims now pending in the application, it is requested that the Examiner telephone Mr. Kin-Wah Tong, Esq. at (732) 530-9404 so that appropriate arrangements can be made for resolving such issues as expeditiously as possible.

Respectfully submitted,

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